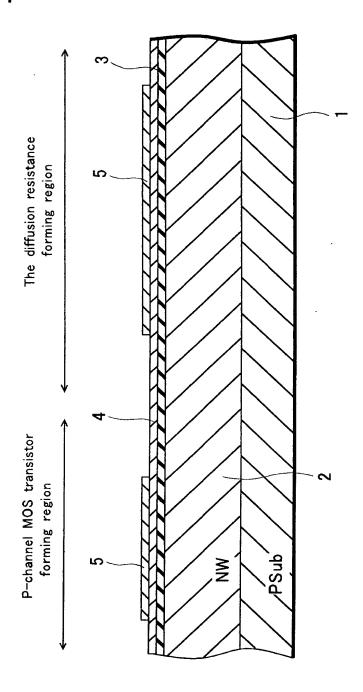
Title: Semiconductor Device and Manufacturing Method Therefor Inventor Duyuki SEKIKAWA et al.
Applica To.: to be assigned
Docket No.: 492322002200

Sheet 1 of 10

1/10

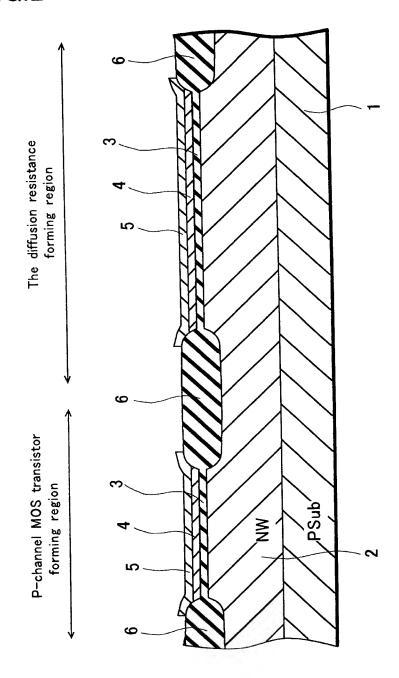
FIG.1



Sheet 2 of 10

2/10

FIG.2

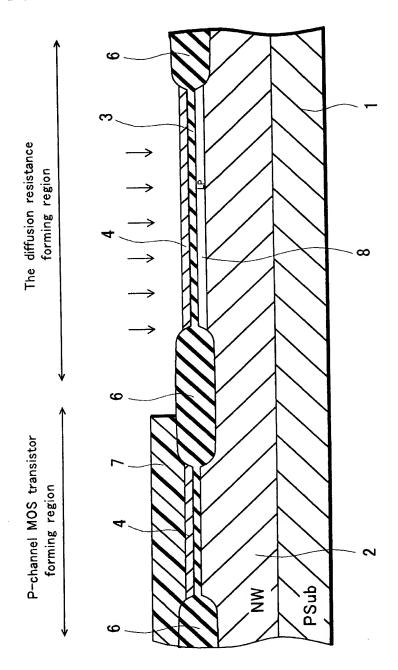


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Sheet 3 of 10

3/10

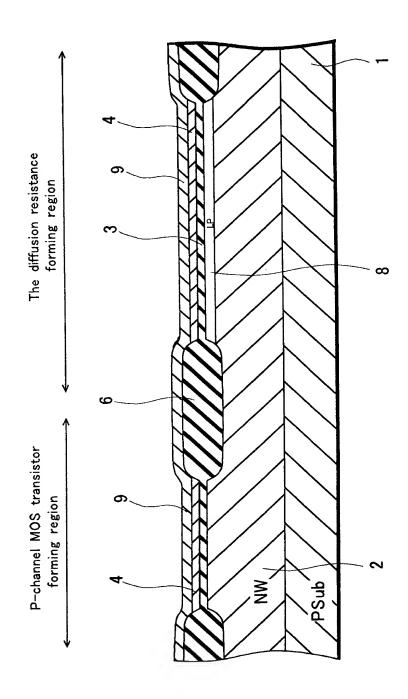
FIG.3



Sheet 4 of 10

4/10

FIG.4



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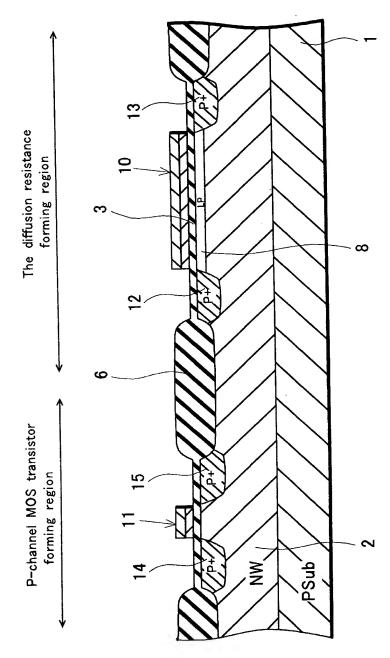
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Applica No.: to be assigned
Docket No.: 492322002200

Sheet 5 of 10

5/10

FIG.5



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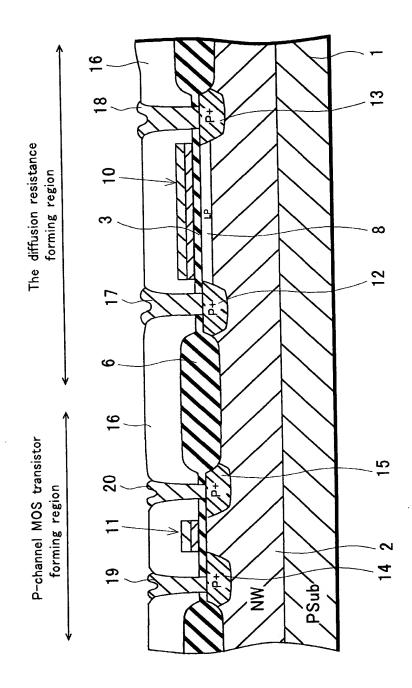
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Application to be assigned
Docket No.: 492322002200

Sheet 6 of 10

6/10

FIG.6



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Sheet 7 of 10

7/10

FIG.7

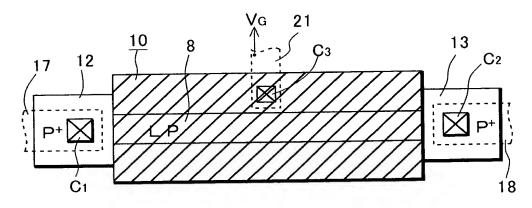
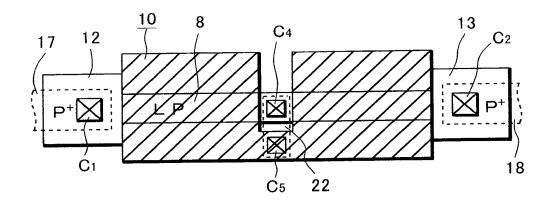


FIG.8



Application : to be assigned Docket No.: 492322002200

Sheet 8 of 10



FIG.9(A)

R=0

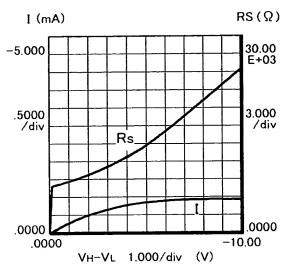


FIG.9(B)

R=0.2

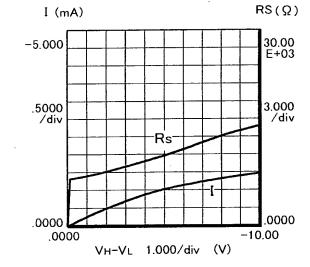
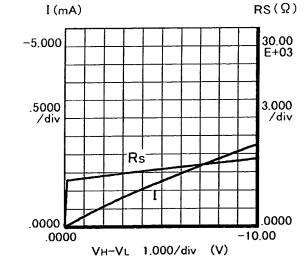


FIG.9(C)

R = 0.4

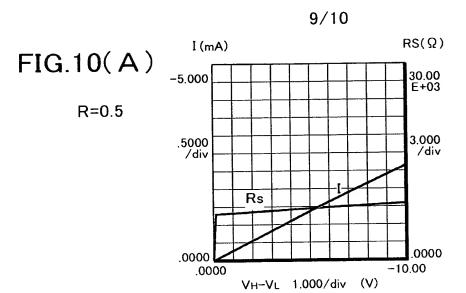


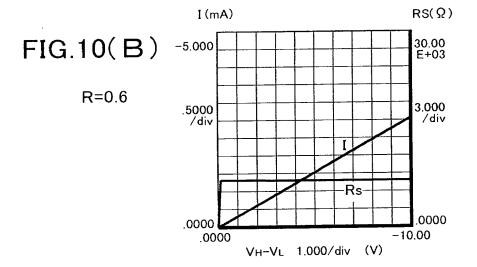
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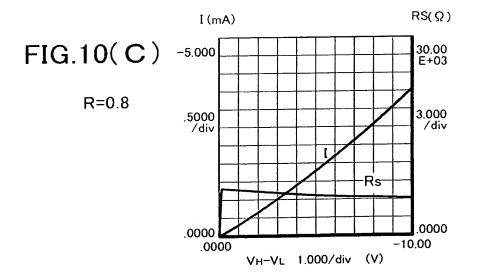
Invente Nobuyuki SEKIKAWA et al.

Appli No.: to be assigned Docket No.: 492322002200

Sheet 9 of 10







Title: Semiconductor Device and Manufacturing Method Therefor

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Approxim No.: to be assigned
Docket No.: 492322002200



10/10

FIG.11

PRIOR ART

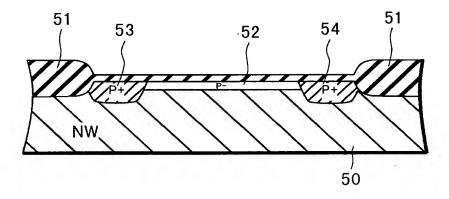
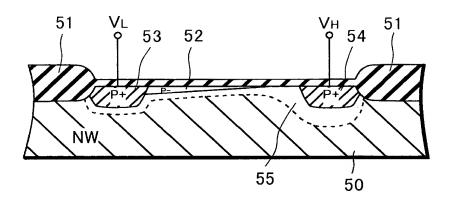


FIG.12

PRIOR ART



Design and the contract of the